



Remedial Classes 2020-21

Department of Electrical and Electronics Engineering

GOKARAJU RANGARAJU
INSTITUTE OF ENGINEERING AND TECHNOLOGY
(Autonomous)

Table of Contents

S.No	Details
1	Circular
2	Schedule of Classes
3	Student Roll List
4	Student Attendance Sheets
5	Faculty Report
6	Student Feedback
7	Photographs
8	Transition Rate Report



GRIET/PRIN/12A/G/20-21

30th May 2021

GOKARAJU RANGARAJU INSTITUTE OF ENGINEERING & TECHNOLOGY

REMEDIAL CLASSES 202-21

CIRCULAR

FINISHING SCHOOL

This is to inform you all that Remedial Classes will be held for academically weak students from May to June 2021. List of students and time tables are send to individual departments.

Dean Finishing School

-

30th May 2021

From
Dean,
Finishing school
GRIET.

To
The HOD
EEE
GRIET

Request for faculty and Class rooms to conduct Remedial classes.

Sir/Madam,

This is to inform you that Finishing school of GRIET is conducting Remedial classes for B.Tech II year & III year students to clear their backlogs of Sem-I.

To conduct the classes we request you

- 1) Permit us to use 2 online classes from 3PM -4PM from 31st May 2021 to 16th June 2021
- 2) Nominate faculty to teach the following courses:

S.No	YEAR	Course title	No. of Students	Name of the faculty	Signature of the faculty
1	II-II	Principles of Digital Electronics (GR18A2084)	21	Mr. P Ravikanth	Mr. P Ravikanth
2	II-II	Control Systems (GR18A2032)	21	Mr. D Karunakumar	Mr. D Karunakumar
3	III-I	Signals and Systems (GR18A2052)	21	Mr. R Anil Kumar	Mr. R Anil Kumar
4	III-I	Power Electronics (GR18A3014)	20	Dr Pakkiraiah B	Dr Pakkiraiah B



Thanking you
Yours Sincerely,
Dr V N Ramadevi



Gokaraju Rangaraju Institute of Engineering and Technology
Electrical and Electronics Engineering
Remedial Classes Schedule

Sl.No	Roll.No	Subject Code	Subject Name
1	17241A0231	GR18A2052	Signals and Systems
2	17241A0299	GR18A2052	Signals and Systems
3	18241A0202	GR18A2052	Signals and Systems
4	18241A0205	GR18A2052	Signals and Systems
5	18241A0209	GR18A2052	Signals and Systems
6	18241A0220	GR18A2052	Signals and Systems
7	18241A0224	GR18A2052	Signals and Systems
8	18241A0244	GR18A2052	Signals and Systems
9	18241A0254	GR18A2052	Signals and Systems
10	18241A0262	GR18A2052	Signals and Systems
11	18241A0280	GR18A2052	Signals and Systems
12	18241A0287	GR18A2052	Signals and Systems
13	18241A0288	GR18A2052	Signals and Systems
14	18241A0290	GR18A2052	Signals and Systems
15	18241A0292	GR18A2052	Signals and Systems
16	18241A0298	GR18A2052	Signals and Systems
17	18241A02A0	GR18A2052	Signals and Systems
18	18241A02A3	GR18A2052	Signals and Systems
19	18241A02A4	GR18A2052	Signals and Systems
20	18241A02A8	GR18A2052	Signals and Systems
21	19245A0210	GR18A2052	Signals and Systems
1	17241A0231	GR18A3014	Power Electronics
2	17241A0299	GR18A3014	Power Electronics
3	18241A0202	GR18A3014	Power Electronics
4	18241A0205	GR18A3014	Power Electronics
5	18241A0209	GR18A3014	Power Electronics
6	18241A0220	GR18A3014	Power Electronics
7	18241A0244	GR18A3014	Power Electronics
8	18241A0248	GR18A3014	Power Electronics
9	18241A0257	GR18A3014	Power Electronics
10	18241A0260	GR18A3014	Power Electronics
11	18241A0262	GR18A3014	Power Electronics
12	18241A0280	GR18A3014	Power Electronics
13	18241A0288	GR18A3014	Power Electronics
14	18241A0290	GR18A3014	Power Electronics
15	18241A0296	GR18A3014	Power Electronics
16	18241A0298	GR18A3014	Power Electronics
17	18241A0299	GR18A3014	Power Electronics
18	18241A02A0	GR18A3014	Power Electronics
19	18241A02A3	GR18A3014	Power Electronics
20	18241A02A8	GR18A3014	Power Electronics
1	17241A0299	GR17A2105	Principles of Digital Electronics
2	18241A0205	GR18A2084	Principles of Digital Electronics
3	18241A0209	GR18A2084	Principles of Digital Electronics

4	18241A0220	GR18A2084	Principles of Digital Electronics
5	18241A0222	GR18A2084	Principles of Digital Electronics
6	18241A0223	GR18A2084	Principles of Digital Electronics
7	18241A0234	GR18A2084	Principles of Digital Electronics
8	18241A0244	GR18A2084	Principles of Digital Electronics
9	18241A0260	GR18A2084	Principles of Digital Electronics
10	18241A0262	GR18A2084	Principles of Digital Electronics
11	18241A0272	GR18A2084	Principles of Digital Electronics
12	18241A0279	GR18A2084	Principles of Digital Electronics
13	18241A0288	GR18A2084	Principles of Digital Electronics
14	18241A0290	GR18A2084	Principles of Digital Electronics
15	18241A0298	GR18A2084	Principles of Digital Electronics
16	18241A02A0	GR18A2084	Principles of Digital Electronics
17	18241A02A3	GR18A2084	Principles of Digital Electronics
18	18241A02B0	GR18A2084	Principles of Digital Electronics
19	18241A02B1	GR18A2084	Principles of Digital Electronics
20	18241A02C0	GR18A2084	Principles of Digital Electronics
21	19245A0210	GR18A2084	Principles of Digital Electronics



Gokaraju Rangaraju Institute of Engineering and Technology

Electrical and Electronics Engineering

Finishing School

Remedial Classes Schedule (**ONLINE**)

31st May 2021 to 19th May 2021

III B.Tech I Sem

S.No	Subject	Year	Name of the Faculty	Session-1	Session-2	Session-3	Session-4	Session-5
1	Principles of Digital Electronics (GR18A2084)	II-II	Mr. P Ravikanth	31/05/2021 (3.00 to 4.00)	5/06/2021 (3.00 to 4.00)	07/06/2021 (3.00 to 4.00)	12/06/2021 (3.00 to 4.00)	14/06/2021 (3.00 to 4.00)
2	Signals and Systems (GR18A2052)	III-I	Mr. R Anil Kumar	01/06/2021 (3.00 to 4.00)	04/06/2021 (3.00 to 4.00)	08/06/2021 (3.00 to 4.00)	11/06/2021 (3.00 to 4.00)	15/06/2021 (3.00 to 4.00)
3	Power Electronics (GR18A3014)	III-I	Dr Pakkiraiah B	02/06/2021 (3.00 to 4.00)	03/06/2021 (3.00 to 4.00)	09/06/2021 (3.00 to 4.00)	12/06/2021 (3.00 to 4.00)	16/06/2021 (3.00 to 4.00)

HOD-EEE

Dean, Finishing School

(Faculty Coordinator)



GOKARAJU RANGARAJU INSTITUTE OF ENGINEERING & TECHNOLOGY

FINISHING SCHOOL

REMEDIAL CLASSES (Academic support for students) Student Feed Back

Branch: EEE

Year: II

Sem: II

Subject: Principles of Digital Electronics (PDE)

Faculty Name: Mr. P Ravikanth

S.No	Item	Feedback
1	Material presented	✓Excellent/Very Good/Good/Average/Below Average
2	Teaching Clarity	✓Excellent/Very Good/Good/Average/Below Average
3	Covering of important topics	Excellent/✓Very Good/Good/Average/Below Average
4	Doubts clarification	Excellent/✓Very Good/Good/Average/Below Average

Suggestions:

Dean Finishing School



GOKARAJU RANGARAJU INSTITUTE OF ENGINEERING & TECHNOLOGY

FINISHING SCHOOL

REMEDIAL CLASSES (Academic support for students) Student Feed Back

Branch: EEE

Year: III

Sem: I

Subject: Power Electronics (PE)

Faculty Name: Dr Pakkiraiah B

S.No	Item	Feedback
1	Material presented	Excellent/✓ Very Good/Good/Average/Below Average
2	Teaching Clarity	Excellent/✓ Very Good/Good/Average/Below Average
3	Covering of important topics	Excellent/✓ Very Good/Good/Average/Below Average
4	Doubts clarification	Excellent/✓ Very Good/Good/Average/Below Average

Suggestions:

V N Ramakrishna

Dean Finishing School

Faculty Report on Subject

Subject: Principles of Digital Electronics

Unit1. Discussed about the Fundamentals of Digital Systems and Logic families

Unit2. Explain about Combinational Digital circuits

Unit 3. Discussed about Sequential Circuits and Systems

Unit4. Explain about A/D and D/A Converters

Unit5: Discussed about Semiconductor Memories and Programmable Logic Devices

II. Previous question papers

III. Notes or PPTs

Faculty Report on Subject

Subject: Signals and Systems

Unit1. Discussed about the Continuous-time Signals and Systems

Unit2. Explain about Fourier Series, Fourier Transform, and Laplace Transform

Unit 3. Discussed about Signal Transmission through Linear Systems

Unit4. Explain about Sampling & Discrete-time Signals

Unit5: Discussed about Z-Transform:

II. Previous question papers

III. Notes or PPTs

Faculty Report on Subject

Subject: Power Electronics

Unit1. Discussed about the Power switching devices

Unit2. Explain about Thyristor rectifiers

Unit 3. Discussed about Three-Phase Voltage Source Inverter

Unit4. Explain about DC-DC Converters

Unit5: Discussed about Single-Phase Voltage Source Inverter

II. Previous question papers

III. Notes or PPTs

Unit-1:

Typical Signals in Continuous Time Domain:

Name of the Signal	Formula	Graph
Unit Impulse Signal	$\delta(t) = \begin{cases} 1 & \text{for } t = 0 \\ 0 & \text{for } t \neq 0 \end{cases}$	
Unit Step Signal	$u(t) = \begin{cases} 1 & \text{for } t \geq 0 \\ 0 & \text{for } t < 0 \end{cases}$	
Unit Ramp Signal	$r(t) = \begin{cases} t & \text{for } t \geq 0 \\ 0 & \text{for } t < 0 \end{cases}$ $r(t) = t u(t)$	

Unit-1: Lesson – 5

Though there are infinite possibility of representing Vectors, possibilities of representing the Vectors are shown below.

Two Vectors and V_e is Error Vector.

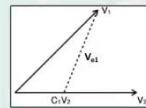


Fig: 1
 $V_1 = C_1 V_2 + V_{e1}$

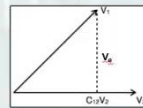


Fig: 2
 $V_1 = C_2 V_2 + V_{e2}$



Fig: 3
 $V_1 = C_2 V_2 + V_{e2}$

Unit-1: Lesson - 6

- Equating $\frac{dE}{dC_{12}} = 0$ we get
 - $\rightarrow \frac{d}{dC_{12}} \left\{ \frac{1}{t_2 - t_1} \int_{t_1}^{t_2} [x_1(t) - C_{12}x_2(t)]^2 dt \right\} = 0$
 - $\rightarrow \frac{d}{dC_{12}} \left\{ \frac{1}{t_2 - t_1} \int_{t_1}^{t_2} [x_1^2(t) + C_{12}^2 x_2^2(t) - 2C_{12}x_1(t)x_2(t)] dt \right\} = 0$
 - Changing the order of integration and differentiation,
 - $\rightarrow \int_{t_1}^{t_2} x_1(t) \cdot x_2(t) dt = C_{12} \int_{t_1}^{t_2} x_2^2(t) \cdot dt$
 - On solving we get (contd..)

Signals and Systems - GR18A2052 : R. Anil Kumar - EEE

Unit-1: Lesson - 7

Orthogonal Signal Space:

- As a Vector **A** can be expressed as *sum of components along a set of 'n' mutually orthogonal vectors*, provided these vectors form a complete set of co-ordinate system.
- Similarly, in the case of Signals, any signal $x(t)$ can be expressed as a sum of its components along a set of ' n ' mutually orthogonal functions, if these functions form a complete set.

Signals and Systems - GR18A2052 : R. Anil Kumar - EEE

Report on Remedial Classes

This is to inform you that Finishing school of GRIET is conducting Remedial classes for B.Tech Ilyear,III year,IV year students to clear their backlogs.

Details are

1. Remedial classes are conducted in different Subjects to support the Students in clearing their backlogs. As the first step, classes are held for Final year and Marched out batches in three different schedules. Students were informed through SMS. Students shown lot of interest .Faculty gave tips as well as material for the students.80-90% of the students who have attended got benefit and they passed in the exams.
2. The classes are aimed to help the students having a maximum of three backlogs so that they will get the degree as per their academic calendar. Students preferred material and few tips as they were busy in Projects. For some subjects they came and attentive.
3. The sessions for II & III-year students are to prevent failure rate and thereby increasing transition rate. The subjects are selected based on I-semester results. To increase attendance for the classes a brief motivation lecture is organized with the key note address by HOD.

The following shows the courses for which Remedial classes are held and the Transition rate in such course:

S.No	Course	No.of students attended	No.of students passed	Transition rate
1.	Principles of Digital Electronics	21	14	66.67
2.	Signals and Systems	21	12	57.41
3.	Power Electronics	20	15	75